



PTO/SB/05B (09-06)

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Substitute for form 1449/PTO		Complete if Known	
		Application Number	10/817,158
		Filing Date	April 2, 2004
		First Named Inventor	FASSIO, Marcelo Daniel Baru
		Art Unit	3762
		Examiner Name	MANUEL, George
Sheet	2	of	3
		Attorney Docket Number	
		<i>..99999-00 3368</i>	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		ALVAREZ; BiCMOS Technology and Applications; 1993; pp. 317-318; 2nd Edition; Kluwer Academic Publishers.	
		ANDRES et al.; "How to Use Nerve Cuffs to Stimulate...", Neural Prothesis; 2001; pp. 139-175.	
		BINKLEY et al.; "A Micropower CMOS, Direct-Coverision..."; IEEE Journal of Solid-State Circuits; March 1998; pp. 344-358; vol. 33; issue 3.	
		DONALDSON et al.; "An Implantable Telemeter For Long-Term..."; Proceedings of the 5th Annual Conference of the International Functional Electrical Stimulation Society.	
		ENZ et al. ; "An Analytical MOS Transistor Model..."; Analog Integrated Circuit Signal Processing; 1995; pp. 83-114; vol. 8.	
		GRAY et al.; "MOS Operational Amplifier Design..."; IEEE Journal of Solid-State Circuits; 1982; pp. 969-982; vol. sc-17; issue 6.	
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		HAUGLAND et al.; "Slip Information Provided by Nerve Cuff..."; IEEE Transactions on Rehabilitation Engineering; 1994; pp. 29-36; vo. 2; issue 1.	
		JINDAL; "Noise Associated with Distributed Resistance..."; IEEE Transactions on Electron Devices; 1984; pp. 1505-1509; vol 31; issue 10.	
		NICOLLINI et al.; "A 3.3-V 800-n rms Noise..."; IEEE Journal of Solid-State Circuits; 1998; pp. 915-921; vol. 28; issue 8.	

Examiner Signature	/George Manuel/	Date Considered	02/09/2008
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		Attorney Docket Number	E 99999-003368

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		NIKOLIC et al.; "Instrumentation for ENG and..."; IEEE Transactions on Biomedical Engineering; 1994; pp. 703-706; vol. 41; issue 7.	
		PAPATHANASIOU et al.; "An Implantable CMOS Signal..."; IEEE International Symposium on Circuits and Systems; 2000.	
		SACKINGER et al.; "A General Relationship Between Amplifier Parameters..."; IEEE Transactions on Circuits and Systems; 1991; pp. 1173-1181; vol. 38; issue 10.	
		SACKINGER et al.; "A Versatile Building Block..."; IEEE Journal of Solid-State Circuits; 1987; pp. 287-294; vol. SC-22; issue 2.	
		SILVERIA et al.; "A gm-lsubD Based Methodology..."; IEEE Journal of Solid-State Circuits; 1996; pp. 1314-1319; vol. 31; issue 9.	
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		STRANGE et al.; "Gait Phase Information Provided..."; IEEE Transactions on Biomedical Engineering; 1999; pp. 797-809; vol. 46; issue 7.	
		VITTOZ; "Micropower Techniques, Design of Analog-Digital VLSI Circuits for Telecommunications..."; 1993; pp. 53-67.	

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